

ANDREW F FITZGERALD

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OBJECTIVE

To continue developing a career in the field of Computer Engineering. Tentatively available near the end of 2009.

EDUCATION

Rochester Institute of Technology Rochester, NY • Master of Science in Computer Engineering, Expected 2009
Cumulative GPA: 3.571/4.000 • Dean's List Spring 2008 through present

Selected Coursework

·Cryptography I & II ·Robotics ·Multiple Processor Systems
·Number Theory ·Grid Computing ·Real-time & Embedded Systems I & III

Thesis work Researching hardware implementations of the Advanced Encryption Standard(AES).
The outcome will be AES implementations targeted for an FPGA that will be evaluated by performance, hardware area, and power consumption.

Rochester Institute of Technology Rochester, NY • Bachelor of Science in Computer Engineering, 2008
Cumulative GPA: 3.242/4.000 • Dean's List Fall 2003, and Fall 2006 through Spring 2008

Selected Coursework

·Electronic Design & Automation ·Digital Signal Processing ·Digital Systems Design
·Interface & Digital Electronics ·VLSI Design ·Operating Systems
·Data & Computer Communication ·Computer Organization ·Computer Architecture

WORK HISTORY

Rochester Institute of Technology, Rochester, NY December 2008-May 2009

Graduate Hardware/Software Co-design Co-op & Research Assistant

- Working on the Blu Lockdown project for Harris Corporation.
- Goal of the project is to secure data transmitted over a Bluetooth connection in real time.
- Developing both hardware and software on an Altera Cyclone III FPGA.

Diamond Packaging, Rochester, NY June 2005-December 2008

Shipping Department

- Performed warehouse management in an industrial setting.
- Job required the usage computerized inventory management systems and a variety of industrial equipment.
- Performed common daily tasks relating to shipping, receiving, delivery, or warehousing.

C Speed, LLC., Syracuse, NY December 2006-August 2007

Engineering Department Co-op

- Created a tool chain of applications that facilitated rapid prototyping on a pick and place machine.
- Adapted software created for a standalone medical device to be used over a SMB network.
- Worked on several other projects ranging from automation programs or scripts to database recovery.

Thomson West, Rochester, NY December 2005-May 2006

Analytical Software Development Co-op

- Development of automated conversion and rendering systems used in the production process of legal research products.
- Work was in a project based environment with specific deliverables assigned for production releases.

SKILLS

- Proficient in development in C, Java, C++, VHDL, HC12 and M68K assembly
- Familiar with:
 - C#, Verilog, several scripting languages, L^AT_EX
 - C Extensions: MPI, NVIDIA CUDA
 - Regular expressions, database design (SQLite, Oracle SQL)
 - Web development, specifically with JSP or Apache Tomcat with Velocity page generation.
 - Hardware/Software Codesign in Xilinx or Altera FPGAs using either a softcore processor(NIOS) or a contained embedded microprocessor(Virtex series with PowerPC).
 - GNU/Linux, Windows, Embedded OS: μ C Linux, QNX, VxWorks
 - Development in Eclipse IDE or MS Visual Studio.

NOTEWORTHY PROJECTS

- Team leader of a senior design project that developed an FPGA based multipurpose driver/data acquisition system. Applications of the project range from robotics testing to IC verification.
- Team member of a robotics project that created a mobile robot to collect objects based on their color and relative distance with the goal returning them to a predefined base within a specified amount of time.

ACTIVITIES AND INTERESTS

- Amateur alto saxophone student and brother in The National Fraternity of Kappa Delta Rho.
- Interested in cryptography, astronomy, automation, and psychology.