ETM MASK

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1-10-2009 ETM_Mask.ppt
INTRODUCTION

This mask was created by Brian Bennamati in 1987 as part of his MS thesis. It has been used at Eastman Kodak and at RIT ever since. This document shows the details of this mask.
LOCATION ON VLSI COMPUTERS

(shared/ume/etm_layout)
**EXPOSURE TEST MASK ETM-1**

<table>
<thead>
<tr>
<th></th>
<th>Cell A</th>
<th>Cell B</th>
<th>Cell C</th>
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<tbody>
<tr>
<td>Cell A – Vertical Lines/Spaces/Holes/Islands</td>
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<tr>
<td>Cell B – 1st Layer for Overlay Evaluation</td>
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<tr>
<td>Cell C – Horizontal Lines/Spaces/Holes/Islands</td>
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<tr>
<td>Cell D – Image Evaluation Lines/Spaces/Checker Boards/ Focus Star/Murray Dagger/ 2nd Layer for Overlay Evaluation</td>
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</tbody>
</table>

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CELL D
CELL A AND C
Jake Leveto Google map of the ETM Mask

http://nulearnova.com/microe/etm.html